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| DOC No | APN-A01005 | Type | Statement | Released Date | 2003/11/27 |
| Model | Chroma 3500 | S/W Version | Craft 1.2.0 ↑ | Issued By | S640-Gary |

I • Subject :

.How to transfer TS6700 main program (*.tdl) to 3500 plan program (*.pln)

II • Descriptions :

the procedure :

1. user must create the information file (*.ifo) by himself . //see [example.ifo](#)

PS:

User must try to find the main test plan , and the test flow will be in the main test plan .

2. **plant2c.exe example.ifo** //automatically output [example.pg](#) & example.pln
3. update example.dec //update information from **example.pg**

PS:

In example.pg , there will have some pins or pin group which are undefined . User must define the pin or pin group in (.dec) .

4. check example.pln //check list : see [plan_check_list.txt](#)

PS : [plant2c not support statement list](#)

III • Example :

plant2c.exe example.ifo <<<

// example.ifo : //

```

SEQ_DEF { //keyword: the TEST_SEQ in G5510 *.t ; the "flow(...)" in TS6700 *.tdl
    begin, //BEFORE_TEST :
        open_short_test, //test function
        ram_max_test,
        freq_test,
        endp, //AFTER_TEST :
}

```

```
// example.pg : //
```

```
POWER_PIN_GROUP {
```

```
    UVI_1_4_PG = ; //the power pins of UVI 1:4 in *.ddf
```

```
    RVI_12_14_PG = ; //the power pins of RVI 12:14 in *.ddf
```

```
}
```

```
LCD_PIN_GROUP {
```

```
    LCD_1014_PG = ; //the lcd pin of dut pin 1014 in *.ddf
```

```
    LCD_1001_1263_Pins = ; //the lcd pins of dut pin 1001:1263 in *.ddf
```

```
}
```

```
UR_PIN_GROUP {
```

```
}
```

```
PIN_GROUP {
```

```
    IO_1_Pin = ; //the io pin of dut pin 1 in *.ddf
```

```
    IO_1_7_Pins = ; //the io pins of dut pin 1:7 in *.ddf
```

```
}
```

```
// plan_check_list //
```

0. check the UVI value if over DPS range $-32V < \text{value} < 32V$

PS:

recheck the machine what you use (3500 ,3503,3360,3520),

If 3520 : DPS range $-40V < \text{value} < 40V$

1. the line number of warning message : see `“.tmp”` file
2. if parser error → check `“.dbg”`, find out that transfer to which line
3. `#define CP` → `int IfDefineCP = 1 ;`
4. `#ifdef CP` → `if (IfDefineCP)`
5. `stm_1` → IO pin number 1, `stm_2` → IO pin number 2
6. `cbit(1,2,17,18) {...` → `SET_CBIT(UR_C1+UR_C2+UR_C17+UR_C18,...`
7. search string “Warning” & “Notice” → double check that

```
// plant2c not support statement list : //
```

```
a. t(1:2) { ...}
```